Analysis and Circuit Modeling of APDs

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Abstract—In this paper a new method for increasing the speed of SAGCM-APD is proposed. Utilizing carrier rate equations in different regions of the structure, a circuit model for the structure is obtained. In this research, in addition to frequency response, the effect of added new charge layer on some transient parameters like slew-rate, rising and falling times have been considered. Finally, by trading-off among some physical parameters such as different layers widths and droppings, a noticeable decrease in breakdown voltage has been achieved. The results of simulation, illustrate some features of proposed structure improvement in comparison with conventional SAGCM-APD structures.

Keywords—Optical communication systems (OCS), Circuit modeling, breakdown voltage; SAGCM-APD

I. INTRODUCTION

THE performance of OCS applied photodiodes has been improved by amending of used materials qualities and device structure developments [1]. Avalanche photodiodes (APDs) in comparison with the PIN photodiodes have the internal gain which causes higher detection sensitivity [2]. In APDs, separation of absorption (A) and multiplication layers (M) causes a lower noise and lower dark current [3] so that, achieving better performance, a narrow bandgap material is used in absorption layer and a wide bandgap one is utilized in multiplication layer. Also, a high electric field applied to the multiplication layer to increase the gain-bandwidth product, and this can be achieved better by using a charge sheet layer of high doping density (C) [4]. In recent years, various structures of the APDs have been presented, in which, absorption and multiplication layers are separated, so that, only one type of carrier is transported into the multiplication region, which reduces the multiplication noise. Also, thinner the multiplication region width and lower breakdown voltage of APDs are most important features of modern OCS and other applications [5]. High-performance APDs are achieved by using separated absorption, grading, charge and multiplication layers (SAGCM) [6]. The gain-bandwidth characteristic is one of the most important parameter of the APDs. The gain-bandwidth characteristic is one of the most important parameter of the APDs and its high quantities, makes it available to work in high bit rates and defines these systems future development stages.

In all pervious works APDs speed criterion was gain-bandwidth product and transient response parameters in term of slew-rate haven’t been considered. A few analysis and circuit models for APDs with separation of absorption and multiplication layers have been reported [7, 8, 9, 10], and also there are only a few attempts to study the frequency and impulse response of the APDs.

In this work, to achieve higher performance device, a new thin charge layer, have been added between absorption and grading layers to obtain a new structure which can be called SAGCM. Furthermore, in addition to gain-bandwidth product consideration, frequency response and effective speed parameters such as slew rate, fall time and rise time have been studies as transient time response. All results show a great improvement in comparison with its predecessors. Furthermore, breakdown voltage of device has been reduced by controlling the electric field in multiplication layer which is the most important features of photo detector.

In section 2 and 3, novel SAGCM structure, its electric field profile and different layers equations and its circuit model are presented. Simulation results and their comparisons with other conventional SAGCM structures are shown in section 4 and finally, conclusion is provided in section 5.

II. ELECTRIC FIELD PROFILE AND RATE EQUATIONS

Adding the charge layer causes an increase in primary transport speed of generated optical carriers from absorption layer to multiplication layer. We name the new charge layer (c1) and the charge layer between grading and multiplication layer (c2). Assume that the light illuminates from p+ region to device so that the current of device has 2 parts; diffusion current which is related to minority carriers in p and n regions and drift current which is created in absorption and multiplication region. Electric field in absorption, first charge layer and grading region is assumed to be uniform also because of strong electric field in multiplication region and a part of second charge layer, impact ionization phenomenon is considered therein in addition, depletion region width in n+ and p+ layer is neglected. We suppose electric field in the second charge region and multiplication region can be assumed fully depleted and modeled by consecutive progressive steps.

![Fig. 1 Electric field of Structure](image-url)
Electric field profile, base on structural parameters and length of different layers can be described by following equations.

\[
E_p(x) = \frac{qN_p}{\varepsilon_p}(x + w_p) \quad w_p \leq x \leq 0
\] (1)

\[
E_{c_1}(x) = \frac{qN_p}{\varepsilon_p}w_p + \frac{qN_m}{\varepsilon_m} - \frac{qN_{c_1}}{\varepsilon_{c_1}}(x - w_m)
\]

\[
E_2(x) = \frac{qN_p}{\varepsilon_p}w_p + \frac{qN_m}{\varepsilon_m}w_m + \frac{qN_{c_1}}{\varepsilon_{c_1}}w_{c_1} + \frac{qN_{c_2}}{\varepsilon_{c_2}}w_{c_2} \quad 0 \leq x \leq w_a
\] (2)

\[
w_a \leq x \leq w_a + wc_1
\]

\[
E_p(x) = \frac{qN_p}{\varepsilon_p}w_p + \frac{qN_m}{\varepsilon_m}w_m + \frac{qN_{c_1}}{\varepsilon_{c_1}}w_{c_1} + \frac{qN_{c_2}}{\varepsilon_{c_2}}w_{c_2} + \frac{qN_{c_3}}{\varepsilon_{c_3}}w_{c_3} \quad w_a + wc_1 \leq x \leq w_a + wc_1 + w_{gr}
\] (3)

\[
w_a + wc_1 + w_{gr} \leq x \leq w_a + wc_1 + w_{gr} + wc_2
\]

\[
E_{c_2}(x) = \frac{qN_p}{\varepsilon_p}w_p + \frac{qN_m}{\varepsilon_m}w_m + \frac{qN_{c_1}}{\varepsilon_{c_1}}w_{c_1} + \frac{qN_{c_2}}{\varepsilon_{c_2}}w_{c_2} + \frac{qN_{c_3}}{\varepsilon_{c_3}}w_{c_3} \quad wc_a + wc_1 + w_{gr} \leq x \leq wc_a + wc_1 + w_{gr} + wc_2 + w_{gr}
\] (4)

\[
w_a + wc_1 + w_{gr} + wc_2 \leq x \leq w_a + wc_1 + w_{gr} + wc_2 + w_{gr}
\]

\[
E_a(x) = \frac{qN_p}{\varepsilon_p}w_p + \frac{qN_m}{\varepsilon_m}w_m + \frac{qN_{c_1}}{\varepsilon_{c_1}}w_{c_1} + \frac{qN_{c_2}}{\varepsilon_{c_2}}w_{c_2} + \frac{qN_{c_3}}{\varepsilon_{c_3}}w_{c_3} + \frac{qN_{c_4}}{\varepsilon_{c_4}}w_{c_4} \quad wc_a + wc_1 + w_{gr} + wc_2 + w_{gr} \leq x \leq wc_a + wc_1 + w_{gr} + wc_2 + w_{gr} + w_m
\] (5)

\[
w_a + wc_1 + w_{gr} + wc_2 + w_m \leq x \leq w_a + wc_1 + w_{gr} + wc_2 + w_m + wc_2 + w_m
\]

Carrier rate equations for this approximate model in various regions are;

\[
p' \text{ region} : \frac{dp'}{dt} = -\frac{n'p'}{\tau_{np}} \cdot \frac{ln_n}{q} \quad \& \quad n_{gr} = 0
\] (8)

\[
n' A \text{ region} : \frac{dp_a}{dt} = \frac{P_{Ga}}{\tau_{p_a}} \cdot \frac{P_a}{\tau_{pta}} \cdot \frac{ln_n}{q}
\] (9)

\[
n C_1 \text{ region} : \frac{dp_{c_1}}{dt} = \frac{P_{Gc_1}}{\tau_{p_{c_1}}} \cdot \frac{P_{c_1}}{\tau_{p_{c_1}}}
\] (10)

\[
n G \text{ region} : \frac{dp_g}{dt} = \frac{P_{Gg}}{\tau_{p_{gr}}} \cdot \frac{P_{gr}}{\tau_{p_{gr}}}
\] (11)

III. EQUIVALENT CIRCUIT MODEL OF STRUCTURE

To achieve circuit model of device, firstly, we need change the physical parameters to circuit parameters. Thus, we employ a constant capacitor value, $C_0$, to change charge to voltage. By dividing carrier charge of regions into $C_0$, potential of carriers in each region will obtain. Taking into account different potentials, carrier combination process and also photo generation rate, we can calculate the equal resistors of regions. With parameters in obtained equations we can rewrite the equations and exploit the circuit parameters. Circuit model of this structure is shown in Fig. 2.
IV. SIMULATION RESULT

Several simulations have been done to confirm our proposed structure. Fig. 3. Shows that by increasing M layer width device gains will increases. Also a GBW growth of 26% is attained.

Fig. 4 illustrate that in smaller M layer thicknesses, the lower breakdown voltage can be achieved. By increasing the M layer width, breakdown voltage increases. As it mentioned in Reference [11], for SAGCM with 0.4um multiplication layer thickness, a breakdown voltage of 60(v) is obtained whereas in this structure, with 0.5um multiplication layer thickness, the breakdown voltage is around 44.2(v).

Fig. 5 show response to optical pulse and various M layer thickness which shows slew rate increment by $C_1$ thickness that can be seen in Figure 6.
It can be seen in Fig. 6, that, with the thicker C$_1$ layer, the output current increases and cause a higher gain. Also with increasing this thickness, the output current has faster response in the respect of more photo carriers’ generation in a time span that can be explained by slow rate.

The detector’s rising and falling time has suitable response because of C$_1$ layer existence and their electric fields control. By adding C$_1$ layer and controlling its electric field, Rising time in comparison with conventional SAGCM improves and it’s a convenient criterion of detector speed increase. The falling time depends on C$_2$ layer thickness and its electric field control. The C$_2$ layer causes faster transferring of photo carriers from A layer to M layer and by controlling the charge in C$_1$ layer this structure can be tunable

This is an advantage of this structure in comparison with conventional SAGCM structures.

V. CONCLUSION

A new structure of APDs by adding a layer between absorption and multiplication region has been introduced. It was shown that this additional layer causes significant improvement in GBW product. Simulation results show 23% improvement in gain-bandwidth product and decreases breakdown voltage in comparison with other conventional SAGCM structures. Also, a circuit model was derived from obtained equations which were obtained and developed from carrier rate equations based on assumption of non-uniform electric field in C$_2$ and M regions.

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REFERENCES